

Bio of Reiner Hartenstein

IEEE fellow, SDPS fellow, FPL fellow

Reiner Hartenstein is CS professor at TU Kaiserslautern and was visiting professor at UC Berkeley. His degrees are from KIT Karlsruhe Institute of Technology, where he later was associate professor. He is consultant, authorized expert and referee.

He implemented the first generalization of the Systolic Array, invented the Shuffle Sort algorithm and is the father of High Performance Reconfigurable Computing (HPRC), and the anti-machine paradigm trailblazing data-stream-based computing, obtaining speed-up factors by 4 orders of magnitude - a decade ahead of the scene. He is the father of KARL, leading design language in the 80ies and backbone of the world's first complete VLSI CAD framework and of the first Term Rewriting demo in top-down EDA, more than 2 decades ahead of the TR scene.

Reiner is founder of three, cofounder of two more international conference series and founder of the Multi University „E.I.S. Projekt“, first Mead-&-Conway effort on the continent (Europe and Asia) - incubator of the huge worldwide EUROCHIP action still running.

He published 14 books and more than 400 technical papers and gave numerous talks, including many invited tutorials and over 200 invited talks and 49 keynote addresses.