

Reiner Hartenstein

(for speaker introduction)

<http://hartenstein.de>

Dr.-Ing. Reiner Hartenstein is CS professor at TU Kaiserslautern and consultant and authorized expert and referee on Information Technology.

He was associate professor at Karlsruhe Institute of Technology, where he had received all academic degrees, and was visiting professor at UC Berkeley.

Reiner Hartenstein is IEEE life fellow, SDPS fellow, FPL fellow. and recipient of several other awards.

His current research is focused on Reconfigurable Computing and Supercomputing, Configware / Software Co-Compilation, and related areas. He is credited to be the father of Reconfigurable Computing.

He is implementer and author of the hardware description language KARL, ground breaking forerunner of VHDL and Verilog, and the backbone of the world's first VLSI design & test framework.

He was the founder of the German Multi University "E.I.S" Project, Germany's contribution to the Mead-&-Conway VLSI design revolution.

He is the founder of three, and, co-founder of two more international conference series still running successfully.

He published 16 books, more than 400 technical papers and gave more than 200 invited talks and more than 20 keynote addresses.

