

CV of Reiner Hartenstein

<http://hartenstein.de>

Dr.-Ing. Reiner W. Hartenstein currently is professor¹ of Computer Science and Engineering at Kaiserslautern University of Technology (TU Kaiserslautern). He has received all his academic degrees from the EE Department at the University of Karlsruhe (TH), where he later became Associate Professor of Computer Science. At Karlsruhe he had been the first graduate student and later a Ph. D. student of Karl Steinbuch, the early pioneer of artificial neuronal networks.

Before joining Kaiserslautern he has worked in character recognition, image processing, digital and hybrid circuits and systems, computer architecture, microprogramming, and high level hardware description languages.

At Kaiserslautern he worked on computer structures, high level hardware description languages and on VLSI design methods and tools. Since about ten years Prof. Hartenstein and his group also works on reconfigurable computing and related compilation techniques. Supported by various funding agencies the achievements of his group are the definition and implementation of

- * the successful hardware design language KARL
- * its graphical companion language ABL
- * the Xputer machine paradigm (data-driven procedural operation principles: counterpart of control-procedural "von Neumann"paradigm),
- * the reconfigurable KressArray family (reconfigurable generalization of the systolic array),
- * co-compilation for configware/software co-design,
- * and other achievements

Prof. Hartenstein, FPL fellow, SDPS fellow and IEEE life fellow, has organized numerous international workshops and conferences as a

- * program chair (10 times), as
- * industrial chair, general chair,
- * steering committee member, as well as many times as a
- * program committee member

Prof. Hartenstein is founder or co-founder of

- * IFIP working group 10.2 (now 10.5)
- * the FPL int'l conference series (Field-programmable Logic and Applications)
- * the PATMOS int'l workshop series on low power VLSI design
- * EUROMICRO,
- * the Reconfigurable Computing Education Workshop series, and,
- * the German Multi University E.I.S.-Project (VLSI-Design).

Many times Prof. Hartenstein has been invited to international conferences as an invited speaker, keynote speaker, embedded tutorial speaker, and as a panelist. He has authored or co-authored 400 papers. and authored, edited or co-edited 16 books.

1. emeritus, but still a member of the department